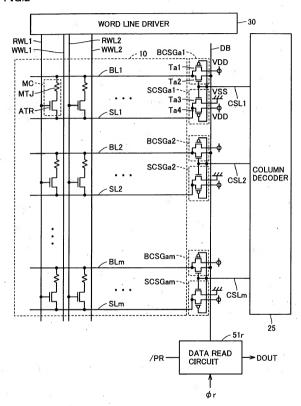


FIG.1

47 Fis

FIG.2





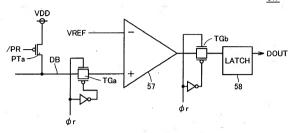
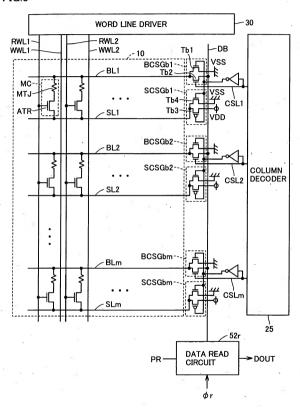
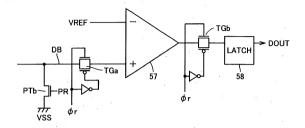


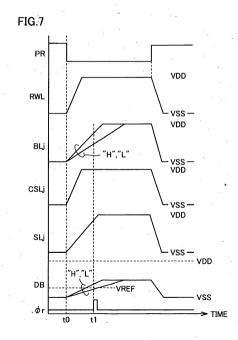
FIG.4 /PR VDD RWL - vss -- vdd -ВLј VSS VDD CSLj - VSS -- VDD -SLj vss DB t0

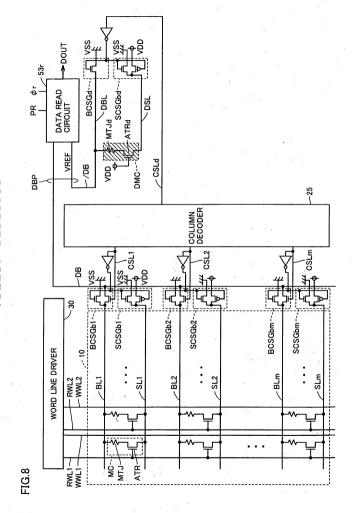
FIG.5

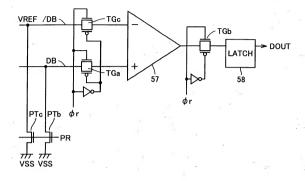


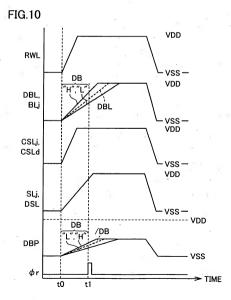
8











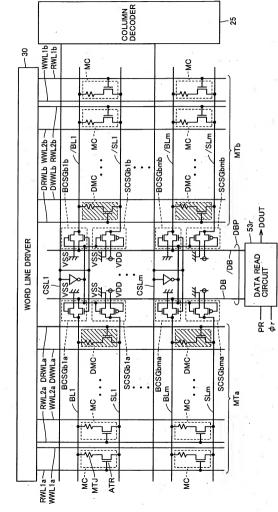
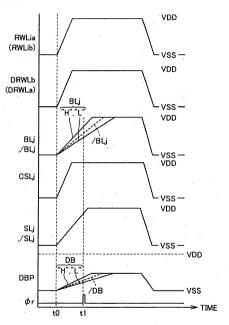
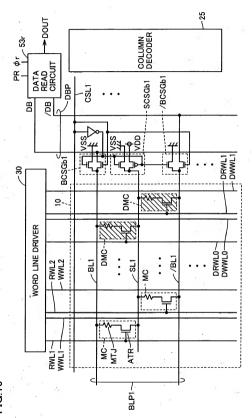


FIG. 11

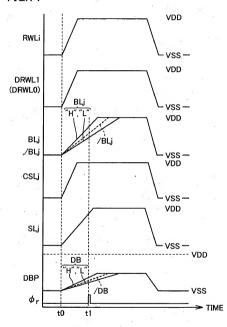
FIG.12

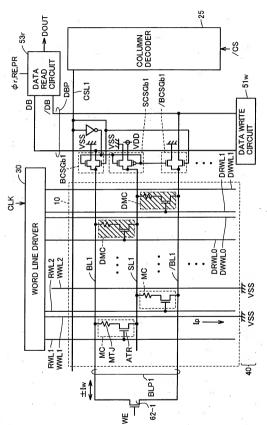




51.5

FIG.14





-1<u>G.15</u>

FIG.16

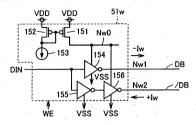


FIG.17

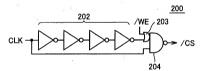


FIG.18

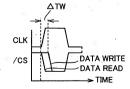


FIG.19

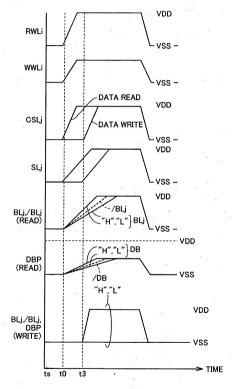


FIG.20

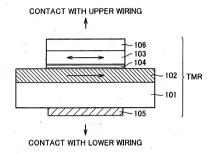


FIG.21

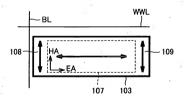


FIG.22

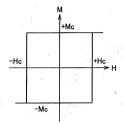
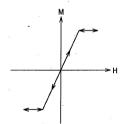


FIG.23



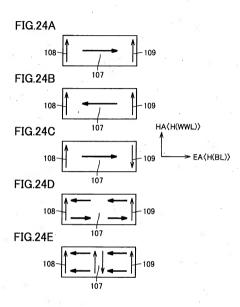


FIG.25

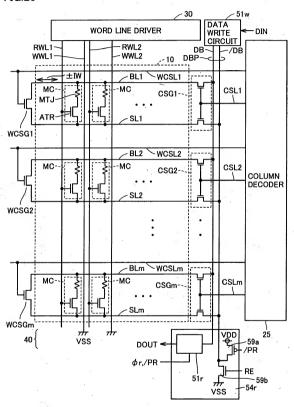
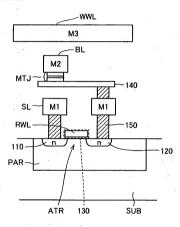


FIG.26



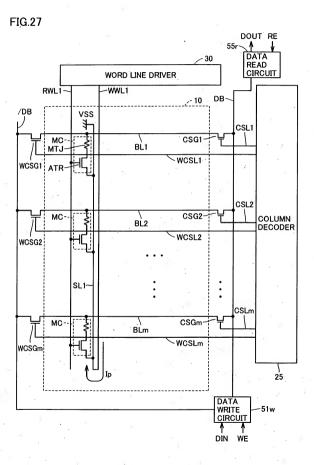


FIG.28

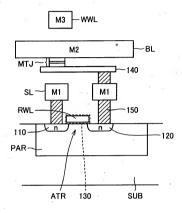
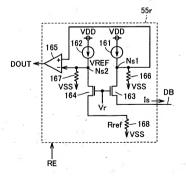
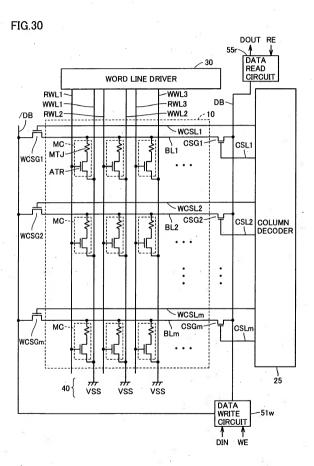
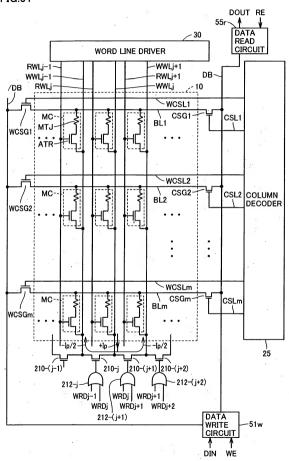


FIG.29







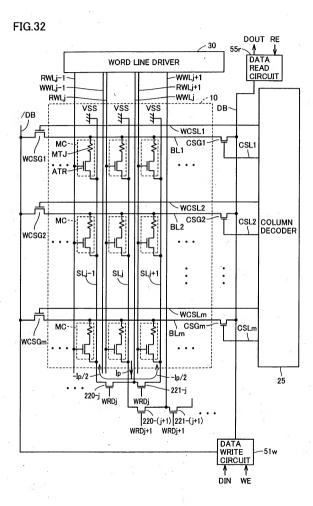


FIG.33

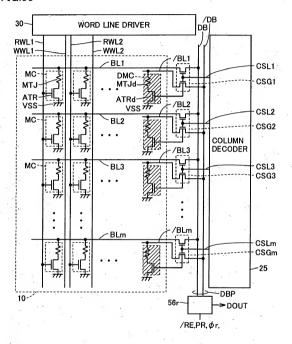


FIG.34

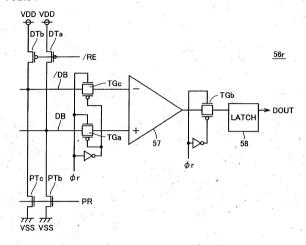


FIG.35

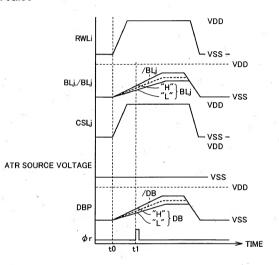
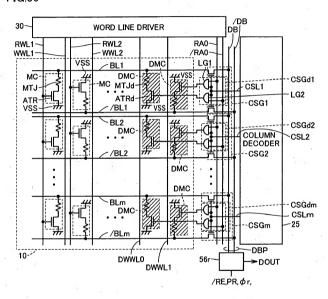


FIG.36



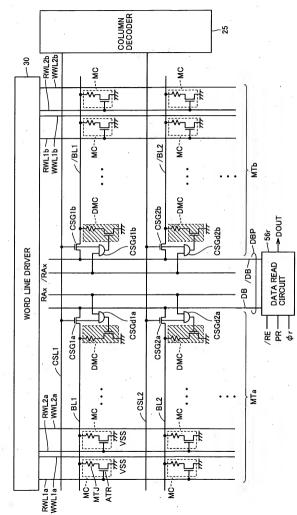
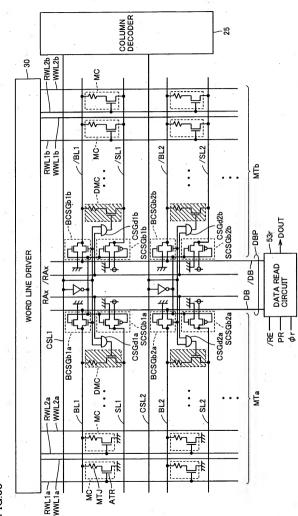


FIG.37



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FIG.39 PRIOR ART

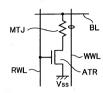


FIG.40 PRIOR ART

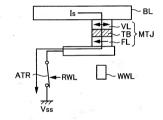


FIG.41 PRIOR ART

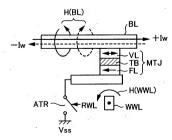


FIG.42 PRIOR ART

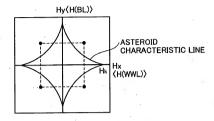


FIG.43 PRIOR ART

